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DEPOSITION OF DOPANT IMPURITIES
AND
PULSED ENERGY DRIVE-IN

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BACKGROUND OF THE INVENTION

5 The present invention relates to the incorporation of dopant impurities, particularly to depositing dopant impurities in the surface of a semiconductor, and more particularly to an enhanced doping process involving deposition of a dopant on the semiconductor surface followed by melting of the surface and drive-in of the dopant using pulsed energy to be
10 absorbed near the surface.

Various techniques have been developed for incorporating a dopant into a material. One of these techniques is known as Gas Immersion Laser Doping (GILD). The GILD technique involves irradiating a material, such as a semiconductor surface, with a pulsed laser in the presence of a dopant
15 ambient (such as BF₃, PF₅, AsH₃, etc.). The GILD process relies on the dopant

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molecules adsorbing on the semiconductor surface, the laser pulse melting a surface region of the semiconductor, the dopant being incorporated into the molten region of the semiconductor, and the molten region solidifying after the laser pulse, the dopant atoms being electrically active after solidification of
5 the semiconductor. Using the GILD technique, the maximum dose of electrically active dopant atoms per pulse is limited to a fraction of the adsorbed dopant molecules (about 10^{13} cm^{-2}). Useful dopant doses are above $5 \times 10^{14} \text{ cm}^{-2}$. Thus, the GILD technique requires at least 50 doping pulses for practical use.

10 The present invention involves a semiconductor doping process which enhances the dopant incorporation achieved using the GILD technique. The doping process of the present invention enhances the doping per pulse by increasing the surface concentration (and possibly thickness) of dopant molecules on the semiconductor surface prior to an energy pulse, thus
15 resulting in a greater dose of dopant per pulse. Thus, by the two-step process of the invention involving: 1) dopant deposition onto a semiconductor surface, and 2) dopant incorporation in the semiconductor by pulsed laser or pulsed ion beam energy, the number of pulses required to achieve useful active dopant concentrations is significantly reduced compared to the GILD technique. The deposition of the dopant on the semiconductor surface can be
20 accomplished by a number of known deposition techniques. The energy pulse may be either that of a pulsed laser or of a pulsed ion-beam source.

SUMMARY OF THE INVENTION

It is an object of the present invention to provide a process for the incorporation of dopant impurities in a material.

A further object of the invention is to provide a doping process which enhances manufacturability of pulsed laser-based semiconductor doping and junction formation.

5 A further object of the invention is to provide a doping process which enhances manufacturability of pulsed ion-beam semiconductor doping and junction formation.

A further object of the invention is to provide a process which enhances the dopant incorporation in a semiconductor.

10 A further object of the invention is to provide a semiconductor doping process which involves deposition of a dopant on a surface of a semiconductor and drive-in of the dopant into the semiconductor by either a pulsed-laser or a pulsed-ion-beam.

15 Another object of the invention is to provide a semiconductor doping process which enhances the dopant incorporation achievable using the Gas Immersion Laser Doping (GILD) technique.

a Another object of the invention is to provide a ~~doping~~ ^{doping} process which enhances the dopant incorporation achievable using pulsed ion-beam doping techniques.

20 Another object of the invention is to provide a doping process which involves depositing a layer of dopant atoms on a surface of a material to be doped, followed by exposure to one or more energy pulses (either laser or ion-beam) which melt the surface thus causing the dopant to be incorporated into the molten region.

25 Another object of the invention is to provide a two-step doping process involving deposition of a dopant on a surface of a material, and

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melting of the surface using pulsed energy (supplied by either a laser or an ion-beam), whereby the dopant is diffused into the surface of the material.

Another object of the invention is to provide a doping process utilizing pulsed laser or pulsed ion-beam processing of a dopant deposited on a semiconductor surface thereby resulting in a greater dose of dopant per energy pulse than the dopant dose produced by the Gas Immersion Laser Doping technique.

Other objects and advantages of the present invention will become apparent from the following description and accompanying drawings. The invention is basically a two-step process for incorporating electrically active dopant atoms into a material, such as a semiconductor, and involves the deposition of dopant impurities on a material surface, and pulsed laser or pulsed ion-beam drive-in of the dopant into the material surface. The

be carried out using
a deposition of the dopant can ~~carry~~ techniques including plasma enhanced chemical vapor deposition (PECVD), also known as glow discharge CVD, sputtering, condensation through cooling the material to be doped, thermal decomposition CVD (such as LPCVD or hot-wire CVD), and photolytic decomposition. The pulsed energy drive-in of the dopant can be carried out, for example, using a 308 nm wavelength XeCl excimer laser with a pulse

a 20 duration or length of 40 ns (below 1 ms) and energy density of 600 mJ cm^{-2} .

The first half of the doping process of the present invention enhances the dopant incorporation achieved using the gas immersion laser doping (GILD) technique. The maximum dose of electrically active dopant atoms per laser pulse produced by the GILD technique is limited to a fraction of the adsorbed molecules (approximately 10^{13} cm^{-2}), with required dopant concentration typically being much higher (for example a minimum of $5 \times 10^{14} \text{ cm}^{-2}$) for a

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source/drain junction in FET type devices, while the dopant dose per pulse of the present invention is greater than 10^{15} cm⁻². Thus, while the GILD technique requires at least 50 doping pulses for practical use, the present invention requires only a few (1 to 10) pulses, thus greatly reducing the 5 required number of pulses and improving the throughput of the doping process.

BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are incorporated into and form a part of the disclosure, illustrate an embodiment of the process of the invention and, together with the description, serve to explain the principles 10 of the invention.

Figure 1 illustrates schematically the dopant deposition operation of the process carried out in accordance with the invention.

Figure 2 illustrates schematically the dopant incorporation following the pulsed laser or ion-beam processing carried out in accordance 15 with the invention.

DETAILED DESCRIPTION OF THE INVENTION

The present invention is directed to a doping process which enhances the dopant incorporation into a material, such as a semiconductor, achievable using doping techniques involving surface melting such as the gas immersion laser doping (GILD) technique. The doping process of this 20 invention is a two-step process for incorporating, for example, an electrically active dopant into a semiconductor. However, the process can be utilized to effectively dope materials for other applications. Because the doping process

of this invention reduces the number of energy pulses required to achieve useful active dopant concentrations, compared to the number of energy pulses required using the GILD technique, the present invention will supersede and possibly replace the current GILD technique. As pointed out 5 above, when using the GILD technique the maximum dose of electrically active dopant atoms per energy pulse is limited to a fraction of the adsorbed molecules (about 10^{13} cm^{-2}). Useful dopant doses are above $5 \times 10^{14} \text{ cm}^{-2}$, and thus the GILD technique requires at least 50 doping pulses for practical use. Using the two-step process of this invention which enhances the dopant 10 surface concentration followed with an energy pulse to incorporate the dopant into the semiconductor, the number of pulses is reduced to a few (1-10) to achieve useful active dopant concentrations. This enhances the manufacturability of pulsed laser and pulsed ion-beam semiconductor doping and junction formation by reducing the required number of energy pulses by 15 a factor of 10.

The invention involves a combination of dopant deposition followed by pulsed laser or ion-beam dopant drive-in. By the process of this invention doping is enhanced by increasing the surface concentration (and possibly thickness) of dopant molecules on the semiconductor surface prior to 20 the energy pulse, thus resulting in a greater dose of dopant per pulse. The first step is to deposit a layer of dopant molecules/atoms on the surface of the semiconductor. The second step is to use either a laser or ion-beam pulse to melt the surface region on the semiconductor. During the molten phase, the dopant molecules/atoms diffuse into the molten region of the semiconductor. Following solidification of the molten region, dopant atoms 25

are frozen into the semiconductor (i.e., incorporated onto lattice sites) and are electrically active.

The two-step doping process is described hereinafter with reference to Figures 1 and 2, wherein Figure 1 illustrates the first step of the process; namely, dopant deposition onto a semiconductor surface; and Figure 2 illustrates the second step of the process; namely, dopant incorporation into the semiconductor by pulsed laser or ion-beam processing. The two-step process increases surface concentration of dopant atoms/molecules compared to prior processes.

For the first step of the process (dopant deposition on a surface of a semiconductor), as seen in Figure 1, a variety of deposition techniques may be used, such as plasma enhanced chemical vapor deposition (PECVD) also known as glow discharge CVD, sputtering, condensation through cooling the semiconductor, thermal decomposition CVD (such as LPCVD or hot-wire CVD), and photolytic decomposition. In PECVD a dopant gas, such as BF_3 , PF_5 , AsH_3 , B_2H_6 , AsF_5 , or PH_3 , or an appropriate organometallic, is introduced into a glow discharge which decomposes the gas into ~~chemical active~~ radicals. These radicals attach to the semiconductor surface and form the dopant layer. By way of example, a glow discharge may be used to decompose PF_5 to deposit a layer of phosphorus atoms/molecules 12 on the surface 13 of {100} silicon 11 of a semiconductor 10, as seen in Figure 1. This could be done by, for example, introducing 0.1 standard cubic centimeters of PF_5 into a glow discharge of He operating at a pressure of 300 nTorr. With 30% conversion efficiency over an area of 1000 cm^2 , this would result in a layer of phosphorus atoms 12 with a surface density of approximately $8 \times 10^{14} \text{ atoms/cm}^2$. This surface density is more than an order of magnitude greater

compared to that achieved by surface adsorption in the GILD technique, and can be easily increased by introducing more PF₅. For sputtering, a standard sputtering technique is used to sputter a target containing the desired dopant to deposit a thin layer of the dopant. For example, magnetron sputtering is a well known technique as exemplified by U.S. Patents No. 5,203,977 and No. 5,232,571. Deposition by condensation is also a well known technique and is implemented by cooling the semiconductor in the presence of a dopant ambient (atmosphere), where the dopant layer is the ^{Condensate} ~~condensant~~. Thermal decomposition CVD is a widely used technique for the desposition of thin films. For example, low pressure CVD (or LPCVD) is often used to thermally decompose SiH₄ or Si₂H₆ gases to form thin films of silicon, and similar techniques could be used to form dopant layers using a dopant source gas (e.g., B₂H₆, AsH₃, PF₅, etc.). Photolytic decomposition is a CVD technique in which a dopant source gas is photolytically decomposed by an appropriate light source (e.g., UV lamp, excimer laser, etc.).

For the second step (dopant incorporation or dopant drive-in), the dopant layer of phosphorus atoms 12 shown in Figure 1 is incorporated or driven into, for example the semiconductor silicon 11, below the surface 13 upon exposure thereto of a pulse of laser or ion-beam energy or laser beam 14 from a source 15, such as, for example, an XeCl excimer laser, having a wavelength of 308 nm. The energy is absorbed in the silicon/dopant layer surface region and converted to thermal energy which melts an upper region of the silicon 11, as indicated at 16, and converts this region to crystalline polysilicon. During the time the silicon region 16 is molten, dopant atoms 12 diffuse rapidly into the silicon as a result of their higher diffusivity. Upon solidification of the silicon region 16, the dopant atoms are incorporated into

the polysilicon region 16, as indicated at 17, in the correct lattice positions to be electrically active sites.

By way of example, an XeCl excimer laser, operating at a wavelength of 308 nm, produces an energy pulse in the range of 50 to 1000 mJ cm^{-2} ,
5 (millijoules per cm^2) with a pulse length of 5 to 100 ns, whereby a dose of about $8 \times 10^{14} \text{ cm}^{-2}$ electrically active dopant atoms per laser pulse is produced. The thickness or depth of the converted polysilicon region 16 is dependent on the energy density and length of the energy pulse(s). Other types of pulsed laser systems may be utilized to produce the desired pulse
10 length and energy density, such may include other types of excimer lasers (e.g. XeF or KF), copper vapor lasers, dye lasers, and pulsed NdYAG lasers. Also, the pulsed energy density can be produced by existing pulsed ion-beam machines.

If the pulsed energy is produced by a laser system, such as
15 exemplified above, a variety of laser wavelengths may be used so long as the wavelength is short enough, whereby the laser energy is absorbed in the near surface region of the silicon, for example. The pulsed duration and energy density can be varied to control the depth of melt, and in turn, the depth of dopant incorporation.

20 It would also be possible to use a pulsed ion beam instead of a pulsed laser beam to deliver energy to the near surface region of the material to be doped. Again, the pulse energy density and duration could be varied to control the depth of melt and doping.

The enhanced doping, made possible by the process of the present
25 invention, will make possible the manufacture of: 1) active matrix flat panel displays on plastic substrates; and 2) shallow junction formation for

microelectronics on silicon, insulating or plastic substrates. Other uses include potential enabling technology for large area low cost electronics (such as flat panel displays), portable electronics, and ultra-submicron (deep submicron) semiconductor device fabrication. Also, by the use of this doping 5 process, flexible displays are envisioned for use in field-deployable portable electronics, battlefield operations facilities, and the interiors of ships, tanks and aircraft. In addition, flexible detector arrays are envisioned for use in radiation (x-ray, gamma ray) detection. Also, shallow junction formation is critical for development use in future supercomputers.

10 It has thus been shown that the doping process of the present invention enhances the dopant incorporation achievable using the GILD technology, and a greater dose of electrically active dopant atoms per laser pulse is produced compared to the GILD technique. The doping process of this invention thus significantly reduces the number of doping pulses require 15 to produce useful dopant doses of above $5 \times 10^{14} \text{ cm}^{-2}$, thereby enhancing the manufacturability of pulsed laser-based as well as pulsed ion-beam-based semiconductor doping and junction formation.

20 While a specific operational sequence, deposition techniques, pulsed energy sources, materials, parameters, etc., have been set forth to exemplify and describe the principles of the invention, such are not intended to be limiting. Modifications and changes may become apparent to those skilled in the art, and it is intended that the invention be limited only by the scope of the appended claims.